



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Confirmation No.: 5084

NOMURA et al.

Art Unit: 2133

Application No.: 09/691,115

Examiner: J. Kerveros

Filed: October 19, 2000

Docket No.: 108066-00014

For: SELF-TEST CIRCUIT AND MEMORY DEVICE INCORPORATING IT

AMENDMENT UNDER 37 C.F.R. 1.111

Assistant Commissioner For Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

June 14, 2004

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Technology Center 2100

Sir:

Responsive to the Office Action dated February 13, 2004, the period for response being extended to June 14, 2004 (June 13, 2004 falling on a Sunday), entry of the following is respectfully requested.